編號: 124 成	位成功大學1	04 學年度碩士班招	<b>性考試試題</b>
系所組別:工程科學系乙、丙	<b>ī</b> 、己組		
考試科目:計算機概論			考試日期:0212,節次:2
第1頁,共2頁			
※考生請注意:本試題不可	「使用計算機。	請於答案卷(卡)作答	,於本試題紙上作答者,不予計分。
	and give the fun	ction or characteristics	. No point will be earned if Chinese
translated only. (5% for e	ach, total 30%)	•	•
(a) NAS;		•	
(b) Pass by reference;			
(c) Memory IO;			
(d) RSS (Really Simple Syn	dication);		
(e) NFC;			
(f) DDoS.			
			•
二、Multiple choices (40%, rig	ght answer will ea	arn 4%, wrong answer v	vill earn -2%. No answer no earns. (答
對得4分,答錯倒扣2分	, 扣至本題零分	<b>}為止。不答不倒扣。</b>	))
(1) Which data structure is	used to assist the	e execution of a subrou	tine call and return? (a) Queue; (b)
Stack; (c) Tree; (d) Gra	oh.		
(2) Which logic gates can b	e used as an odd	parity generator? (a) X	OR; (b) OR; (c) XNOR; (d) NOR.
			for the track of the second second
(3) An one dimensional arr	ay is used to imp	lement a complete bina	ary tree for storing the sequence
A,B,C,D,E,F,G,H,I,J,	hen who is the s	IDling node of E? (a) B;	(b) F; (c) G; (d) D.
(4) Which of the following	is not considered	l as a narameter by Oo	(2) (a) bandwidth: (b) time delay: (c)
(4) which of the following	arrived packet	as a parameter by Qu	(a) bandwidth, (b) time delay, (c)
reliability, (d) order of	arriveu packet.		
(5) A DDR SDRAM chin can	transfer data fas	ter than that of SDRAM	What is the reason? (a) It uses
pipeline technology: (b	) It operates sync	chronously with system	clock: (c) Twice times are transferred
per clock cycle: (d) It is	usually not need	ed to re-energize.	
(6) Two computer systems	called A and B w	ith the same instruction	n set architecture and using the same
assembler. The clock c	cle time and ave	rage cycles per Instruct	ion (CPI) of A are 0.25ns and 2.0,
respectively. And that	of B is 0.5ns and	1.2, respectively. To con	npare the performance of the two
systems, which one is o	correct? (a) It can	not be compared; (b) A	is better than B; (c) B is better than A;
(d) Both of A and B are	with the same p	erformance.	
(7) Which one is not the n	ecessary conditio	n for a deadlock? (a) N	preemption: (b) Mutual exclusion: (c)

Hold and wait; (d) Bounded waiting.

## 國立成功大學 104 學年度碩士班招生考試試題

系所組別:工程科學系乙、丙、己組 考試科目:計算機概論 第2頁,共2頁

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- (8) What is the data unit used in the second layer of OSI? (a) Packet; (b) Segment; (c) Frame; (d) Message.
- (9) Which one is not the software developing model? (a) Waterfall approach; (b) Iterative development;(c) Distributed approach; (d) Component based software engineering.
- (10) A flip flop with two inputs A and B, and the inputs and its corresponding output (AB →output) are 00 → set, 01 → no change, 10 → complement, 11 → clear. If the output state of the flip flop will be changed from 0 to 1, then the inputs are (a) A=0, B=d (don't care); (b) A=d, B=0; (c) A=1, B=d; (d) A=d, B=1.
- Assume N positive integers (each with 7 digits) are stored in a file called "DATAFILE". Write a program with C programming language, to read these data from the "DATAFILE", then to calculate the average value of these integers, and print out the average value on the screen. You can describe how the integers are stored in your "DATAFILE" firstly. Give proper comments to increase your program readability. (15%)
- Merge sort is one of the sorting techniques. Assume there are N integers to be sorted by merge sort technique. Design a merge sort algorithm called MERGE\_SORT to sort these N integers. (a) By recursive method (5%); (b) By iteration method. (10%)