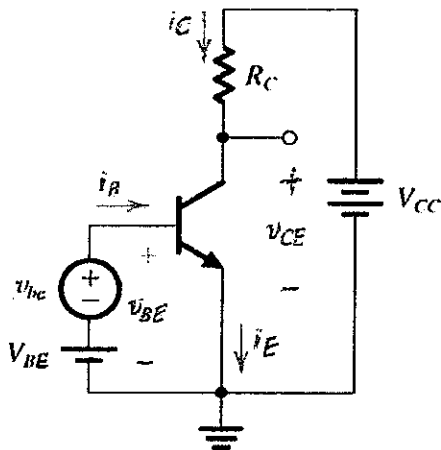


※考生請注意：本試題可使用計算機。請於答案卷(卡)作答，於本試題紙上作答者，不予計分。

1. For the BJT as an amplifier with $V_{CC} = 5\text{ V}$, $R_C = 3\text{ k}\Omega$, and $\beta = 100$, V_{BE} is adjusted so that $V_C = 2\text{ V}$. A signal $v_{be} = 5 \sin(\omega t)\text{ mV}$ is applied. Find the instantaneous response $i_c(t)$ (5%), $v_c(t)$ (5%) and the voltage gain (5%).

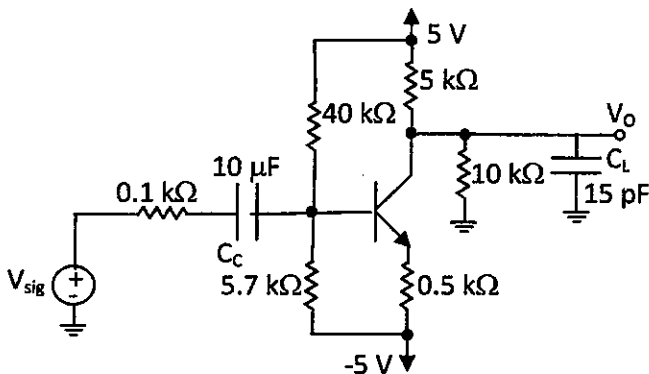


2. For an n-channel MOSFET, given the threshold voltage $V_{tn} = 0.3\text{ V}$, conductance parameter $K_n = (1/2 * W/L * \mu_n C_{ox}) = 12.5\text{ mA/V}^2$: (a) At $V_{GS} = 0.8\text{ V}$, $V_{DS} = 0.05\text{ V}$, the transconductance ($g_m = \frac{\partial I_D}{\partial V_{GS}}$) = ? (A/V) (5%)(b) At $V_{GS} = 0.8\text{ V}$, $V_{DS} = 1.5\text{ V}$, the transconductance = ? (A/V) (5%).

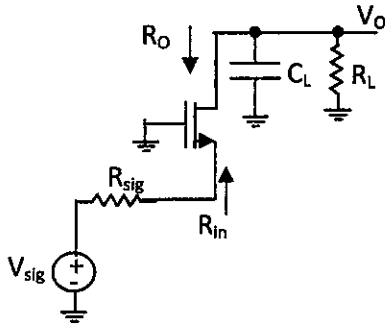
3. For an nMOS transistor fabricated in a $0.25\text{ }\mu\text{m}$ CMOS process with $\mu_n = 400\text{ cm}^2/\text{V}\cdot\text{s}$, $\mu_n C_{ox} = 115\text{ }\mu\text{A/V}^2$, $V_{tn} = 0.5\text{ V}$, $\lambda_n = 0.06\text{ V}^{-1}$, $W/L = 1.5$ and $V_{DD} = 2.5\text{ V}$:

(a) Let $L = 0.25\text{ }\mu\text{m}$ and assume saturation velocity $v_{sat} = 10^7\text{ cm/s}$. Find V_{DSsat} . (5%)
 (b) Continue (a), let $V_{GS} = V_{DS} = V_{DD}$, calculate the drain current (in velocity saturation). (10%)

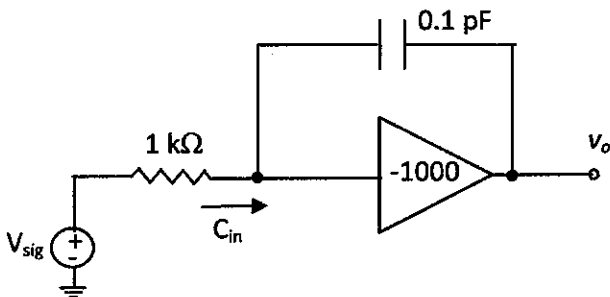
4. For a CE amplifier with a coupling capacitor C_C and a load capacitor C_L . Given $I_C = 1\text{ mA}$ and $\beta = 100$. You may neglect the effect of r_o and r_x . Find the bandwidth ($f_H - f_L$) (Hz). (10%) Hint: $r_\pi = \beta/g_m$.



5. For a common gate amplifier having $g_m = 1.25 \text{ mA/V}$, $r_o = 20 \text{ k}\Omega$, $C_L = 15 \text{ fF}$, $R_L = 20 \text{ k}\Omega$, $R_{sig} = 10 \text{ k}\Omega$, $C_{gs} = 20 \text{ fF}$, and $C_{gd} = 5 \text{ fF}$, (a) find the mid-band voltage gain (10%) and (b) use the method of open-circuit time constant to find the 3-dB frequency f_H (Hz) (10%). Hint: $R_{in} = (r_o + R_L)/(1 + g_m r_o)$, $R_o = r_o + (1 + g_m r_o)R_{sig}$.



6. The following figure shows an ideal voltage amplifier with a gain of -1000 V/V . (a) Use Miller's theorem to find the input capacitance (C_{in}) of the amplifier. (10%) (b) Find the 3-dB frequency f_H (Hz) of the transfer function (V_o/V_{sig}). (5%)



7. A two-stage CMOS op amp is found to have a capacitance between the output node and ground of 1 pF . If the op amp should have a unity gain bandwidth f_t of 100 MHz with a phase margin of 75° , what must g_{m6} be set to? (10%) Assume that a resistance R is connected in series with the frequency-compensation capacitor C_c and adjusted to place the transmission zero at infinity. What is the value of R ? (5%)

