

# 國立成功大學

## 115學年度碩士班招生考試試題

編 號： 140

系 所： 電機資訊學院-資訊聯招

科 目： 計算機數學

日 期： 0203

節 次： 第 3 節

注 意： 1.不可使用計算機  
2.請於答案卷(卡)作答，於  
試題上作答，不予計分。

—、Discrete Mathematics (50%) You should show how to get the answers in detail or obtain no credit.

1.(10%) Consider all simple (loop-free), undirected graphs on a set of **5 distinct labeled vertices**  $V = \{1,2,3,4,5\}$ . Every edge connects two distinct vertices, and multiple edges are not allowed. Assume that **no vertex is isolated** (i.e., every vertex has degree at least 1).

(a) How many such graphs have **exactly one connected component** (i.e., the graph is **connected**)?(b) How many such graphs have **exactly two connected components**?

2.(10%) A network engineer is designing a **fault-tolerant routing backbone** for a data-center fabric. The backbone consists of **20 routers**, arranged physically in a **4×5 cylindrical grid**: There are 4 horizontal layers (“rings”), each containing 5 routers. Horizontally, each layer forms a **5-router cycle** (i.e., the leftmost and rightmost routers are connected). Vertically, each of the 5 columns also forms a **4-router cycle** (i.e., the top and bottom switches are connected). Thus, every router has degree 4: two connections within its horizontal ring, and two within its vertical ring. **Determine whether** this cylindrical multi-ring backbone  $H$  **can be embedded on a flat PCB without wiring intersections. Justify your answer using only Euler’s formula and girth-based edge bounds.** In graph theory, the girth of an undirected graph is the length of the shortest cycle contained in the graph

3.(10%) A self-organizing **ad-hoc satellite network** consists of multiple satellites placed at arbitrary integer coordinates in 3-dimensional space. Each satellite is modeled as a point  $A_i \in \mathbb{Z}^3$ . A **secure communication link** may be established between satellites  $A_i$  and  $A_j$  only if the **midpoint** of the segment  $A_i A_j$  also lies on an integer lattice point. Equivalently, all three coordinate-wise averages must be integers:

$$\frac{x_i + x_j}{2}, \frac{y_i + y_j}{2}, \frac{z_i + z_j}{2} \in \mathbb{Z}.$$

The Space Agency wants to guarantee that even without knowing the positions of the satellites beforehand, **there must exist at least one secure link**.

(a) Determine the **smallest integer**  $N$  such that: No matter how the  $N$  satellites are placed in  $\mathbb{Z}^3$ , **there is always at least one pair** whose midpoint is a lattice point (i.e., at least one secure link always exists).

(b) Now modify the secure-link condition:

A link is allowed if **the total coordinate sum of the two satellites is even**, i.e.

$$x_i + x_j + y_i + y_j + z_i + z_j \equiv 0 \pmod{2}.$$

Under this new rule, determine the **new minimum number of satellites** guaranteeing that at least **one secure link** must exist.

4.(10%) Consider the recurrence:  $E_n = 4E_{n-1} - 4E_{n-2} + 2^n + 3^n$ , with initial values,  $E_0 = 2, E_1 = 10$ . Estimate the **smallest**  $n$  such that  $E_n > 10^9$ .

5. (10%) You are designing the firmware logic for a high-security electronic lock. The lock receives a stream of binary signals (0 or 1) from a keypad. You need to **draw the state transition diagrams for two different versions of the security protocol. Use Moore Machine model for both versions.** Specifically, use circles to represent states with labels  $S_i$  where  $i$  is the index starting from 0, use arrows to represent transitions, use an arrow to point to the start state, and mark the "Unlock" state as double circles.

(a) The Standard "Header-Trailer" Protocol Specification

1. Start-up Check: When the system first turns on, it must check for a specific "Header." The very first two signals received must be a 1 followed by another 1.
2. Security Violation: If the first two signals are anything other than a pair of 1s, the system detects a hack. It must immediately enter a Permanent Lockout state where it ignores all future input and never unlocks.
3. Monitoring: Once the valid header (11) is received, the system waits. It ignores the specific pattern of the signals in the middle.
4. Unlocking: The door should unlock if and only if the last two signals received were a 0 followed by another 0.
5. Relocking: If the system receives more signals after unlocking that break the "00" ending, the door must lock again and go back to monitoring for a new "00" ending.

(b) The Advanced "Strict Rhythm" Protocol Specification

We request a security update to the logic designed in (a). Please modify your Finite State Machine to incorporate the following three specific changes. All other behaviors (like the "11" start-up check) remain the same.

1. Enforce Alternating Data: Instead of ignoring the signals between the header and the unlock command, the system must now strictly enforce an alternating "0, 1, 0, 1..." pattern.
2. Immediate Lockout on Error: If this alternating rhythm is broken (for example, if two 1s appear consecutively after the header), the system must immediately enter the Permanent Lockout state.
3. One-Time Access: The "Relock" feature is removed. Once the door unlocks (upon receiving the "00" suffix), any further input signals are considered a security violation and must trigger the Permanent Lockout state.

二. Linear Algebra (50%)

6. By factoring  $A$  into  $LU$ , the  $LU$  decomposition allows solving  $Ax = b$  by first solving  $Ly = b$ , followed by  $Ux = y$ , each requiring only a single triangular solve. Thus, computing  $A = LU$  is crucial in large-scale computer engineering applications.

(a) Give the complexity of the classic  $LU$  decomposition, shown in Algorithm 1. (10%)

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**Algorithm 1**  $LU$  Decomposition Without Pivoting

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**Require:** Matrix  $A \in \mathbb{R}^{n \times n}$

**Ensure:** Lower triangular matrix  $L$ , Upper triangular matrix  $U$  such that  $A = LU$

- 1: Initialize  $L \leftarrow I_n, U \leftarrow 0_{n \times n}$
  - 2: for  $k = 1$  to  $n$  do
  - 3:     for  $j = k$  to  $n$  do ▷ Compute  $k$ -th row of  $U$
  - 4:          $U_{k,j} \leftarrow A_{k,j} - \sum_{s=1}^{k-1} L_{k,s} \cdot U_{s,j}$
  - 5:     end for
  - 6:     for  $i = k + 1$  to  $n$  do ▷ Compute  $k$ -th column of  $L$
  - 7:          $L_{i,k} \leftarrow \frac{1}{U_{k,k}} \left( A_{i,k} - \sum_{s=1}^{k-1} L_{i,s} \cdot U_{s,k} \right)$
  - 8:     end for
  - 9: end for
  - 10: return  $(L, U)$
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(b) An improvement of (a) based on the block matrix multiplication is shown in Algorithm 2. Give the complexity of the speed-improved  $LU$  decomposition. (10%)

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**Algorithm 2** Speed-Improved Block  $LU$  Decomposition

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**Require:** Matrix  $A \in \mathbb{R}^{n \times n}$ ,  $n$  is a power of 2

**Ensure:** Matrices  $L, U$  with  $A = LU$

- 1: function SPEEDIMPROVEDBLOCKLU( $A$ )
  - 2:      $n \leftarrow$  size of  $A$
  - 3:     if  $n = 1$  then
  - 4:         return  $([1], [A_{11}])$
  - 5:     end if
  - 6:     Partition  $A$  into blocks:  $A = \begin{bmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{bmatrix}$
  - 7:      $(L_{11}, U_{11}) \leftarrow$  SPEEDIMPROVEDBLOCKLU( $A_{11}$ )
  - 8:      $U_{12} \leftarrow$  SISOLVE( $L_{11}, A_{12}$ , lower)
  - 9:      $L_{21} \leftarrow$  SISOLVE( $U_{11}, A_{21}$ , upper)
  - 10:      $S \leftarrow A_{22} -$  SIMULTIPLY( $L_{21}, U_{12}$ )
  - 11:      $(L_{22}, U_{22}) \leftarrow$  SPEEDIMPROVEDBLOCKLU( $S$ )
  - 12:     Construct:  $L = \begin{bmatrix} L_{11} & 0 \\ L_{21} & L_{22} \end{bmatrix}, U = \begin{bmatrix} U_{11} & U_{12} \\ 0 & U_{22} \end{bmatrix}$
  - 13:     return  $(L, U)$
  - 14: end function
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1: function SISOLVE(T, B, type)
2:   if  $n = 1$  then
3:     return  $B/T$ 
4:   end if
5:   Partition T, B into blocks:

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$$T = \begin{bmatrix} T_{11} & T_{12} \\ T_{21} & T_{22} \end{bmatrix}, B = \begin{bmatrix} B_1 \\ B_2 \end{bmatrix}$$

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6:   if type = lower then
7:      $X_1 \leftarrow \text{SISOLVE}(T_{11}, B_1)$ 
8:      $X_2 \leftarrow \text{SISOLVE}(T_{22}, B_2 - T_{21}X_1)$ 
9:   else
10:     $X_2 \leftarrow \text{SISOLVE}(T_{22}, B_2)$ 
11:     $X_1 \leftarrow \text{SISOLVE}(T_{11}, B_1 - T_{12}X_2)$ 
12:   end if
13:   return  $\begin{bmatrix} X_1 \\ X_2 \end{bmatrix}$ 
14: end function

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1: function SIMULTIPLY(A, B)
2:   if  $n = 1$  then
3:     return  $A \cdot B$ 
4:   end if
5:   Partition A, B into:

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$$A = \begin{bmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{bmatrix}, B = \begin{bmatrix} B_{11} & B_{12} \\ B_{21} & B_{22} \end{bmatrix}$$

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6:    $M_1 \leftarrow (A_{11} + A_{22})(B_{11} + B_{22})$ 
7:    $M_2 \leftarrow (A_{21} + A_{22})B_{11}$ 
8:    $M_3 \leftarrow A_{11}(B_{12} - B_{22})$ 
9:    $M_4 \leftarrow A_{22}(B_{21} - B_{11})$ 
10:   $M_5 \leftarrow (A_{11} + A_{12})B_{22}$ 
11:   $M_6 \leftarrow (A_{21} - A_{11})(B_{11} + B_{12})$ 
12:   $M_7 \leftarrow (A_{12} - A_{22})(B_{21} + B_{22})$ 
13:   $C_{11} \leftarrow M_1 + M_4 - M_5 + M_7$ 
14:   $C_{12} \leftarrow M_3 + M_5$ 
15:   $C_{21} \leftarrow M_2 + M_4$ 
16:   $C_{22} \leftarrow M_1 - M_2 + M_3 + M_6$ 
17:   $C \leftarrow \begin{bmatrix} C_{11} & C_{12} \\ C_{21} & C_{22} \end{bmatrix}$ 
18:  return C
19: end function

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(c) Given  $A = \begin{bmatrix} 4 & 3 \\ 6 & 3 \end{bmatrix}$ , compute the LU decomposition  $A = LU$  using the speed-improved LU decomposition method in (b). (10%)

(Note: List the key intermediate results from Algorithm 2 to receive full credit.)

7. Let  $A \in \mathbb{R}^{m \times n}$  with  $m > n$ , and assume that  $A$  is rank-deficient, i.e.,  $\text{rank}(A) < n$ .  $A^T A$  is positive semi-definite.

(a) Let  $\lambda > 0$ . Show that the matrix  $A^T A + \lambda I$  is positive definite. (10%)

(b) Derive the closed-form solution for

$$x^* = \arg \min_{x \in \mathbb{R}^n} \|Ax - b\|^2 + \lambda \|x\|^2, \quad \lambda > 0. \quad \text{span style="border: 1px solid black; padding: 2px;">(10%)}$$