共 ろ 頁 , 第 / 頁

編號:

282 系所:資訊工程學系

科目:計算機組織與系統

本試題是否可以使用計算機: ①可使用 , □不可使用 (請命題老師勾選)

## 「作業系統」

共五題,請在<u>答案卷</u>畫一個表格(如下所示),並在每個空格內<u>清楚地</u>填入五個題目的答案(共17格需要填),以利評分。謝謝!

題目						
	[1]		[2]		[3]	
_	[4]	[5]		[6]		[7]
Ξ	[8]	[9]	[10]	[11]	[12]	[13]
四	[14]			[15]		
<b>五</b> .	[16]			[17]		

- 一. A solution to the critical-section problem must satisfy three requirements. Write down three requirements in English. (此題以英文作答,分別填入第一題的[1], [2], [3]格中, 9%)
- Suppose that the head of a moving-head disk with 150 tracks, numbered 0 to 149, is currently serving a request at track 120 and has just finished a request at track 115. The queue of requests is kept in the FIFO order:

89, 110, 20, 45, 139

Write down the total number of head movements for (4) FCFS (5) SCAN (6) LOOK (7) SSTF scheduling, respectively. (答案需分別填入第二題的[4], [5], [6], [7]格中, 12%)

# (背面仍有題目.請繼續作答)

### 國立成功大學九十六學年度碩士班招生考試試題

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=. There are four processes that arrived at a computer at different time. The arrival time, burst time, and the priority of each process is as the following table: (the time unit is millisecond and a lower priority number means higher priority) (18%)

Process	Arrival time	Burst time	Priority
Α	2	6	4
В	0	9	3
C	3	10	2
D	5	. 5	1

Determine the average turnaround time of **process B** for (8) Preemptive priority scheduling (9) Preemptive shortest-job-first scheduling (10) Nonpreemptive priority scheduling (答案分別填入第三題[8], [9], [10]格中)。 Determine the average turnaround time of **process C** for (11) First-come, first-served scheduling (12) Preemptive priority scheduling (13) Nonpreemptive priority scheduling (答案分別填入第三題[11], [12], [13]格中)

(Ignore process switching overhead and assume that only a process runs at a time)

四.	(a) The objective of multiprogramming is to maximiz	e (此題以英文
	作答,答案填入第四題的[14]格中, 3%)	

- (b) Switching the CPU to another process requires performing a state save of the current process and a state restore of a different process. This task is known as a (此題以英文作答,答案填入第四題的[15]格中,3%)
- 五. (a) In paging scheme, the access of page table causes the decrease in system performance. A standard solution is to use a special, small, fast-lookup hardware cache, called a \_\_\_\_\_\_\_ (此題以英文作答,答案填入第五題的[16]格中,3%)
  - (b) In a multiprocessor system, if all processors are peers (no master-slave relationship exists between processors), we call it \_\_\_\_\_\_ multiprocessing. (此題以英文作答,答案 填入第五題的[17]格中, 2%)

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### 「計算機組織」

共四題,請在答案卷畫一表格如下,並清楚地填入這四個題目的答案(共需填入5格)。

題號	答案		
六	子題(1): 子題(2):		
セ			
入			
九			

- ⇒. Consider a MIPS processor with an additional floating point unit. Assume functional unit delays in the processor are as follows: memory (2 ns), ALU and adders (2 ns), FPU add (8 ns), FPU multiply (16 ns), register file access (1 ns), and the remaining units (0 ns). Also assume instruction mix as follows: loads (31%), stores (21%), R-format instructions (27%), branches (5%), jumps (2%), FP adds and subtracts (7%), and FP multiplys and divides (7%).
  - (1) What is the delay in nanosecond to execute a load, store, R-format, branch, jump, FP add/subtract, and FP multiply/divide instruction in a single-cycle MIPS design? [10%]
  - What is the averaged delay in nanosecond to execute a load, store, R-format, branch, jump, FP add/subtract, and FP multiply/divide instruction in a multicycle MIPS design? [10%]
- tially. How many cache misses will be introduced by the *directed mapped*, 2-way set associative and fully associative caches if the memory blocks with addresses 0, 8, 0, 6 and 8 are fetched sequentially? [10%]
- A. Which of the following techniques can resolve control hazards? [10%]
  - (1) Branch prediction
  - (2) Stall
  - (3) Delayed branch
- 九. Write a C program which exhibits the temporal and spatial localities. The C program cannot exceed 5 lines. [10%]